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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Application:

Art Unit: 2819

Serial No.: 09/800,145

Filed: March 6, 2001

For: **Recursive Cryptoaccelerator and Recursive VHDL Design of Logic Circuits**

Box Patent Application
Assistant Director for Patents
Washington, DC 20231

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INFORMATION DISCLOSURE STATEMENT

In accordance with 37 C.F.R. 1.56, counsel wishes to make of record the attached items of information for the Examiner's consideration in connection with this application. Each item of information contained in this Information Disclosure Statement was cited in a communication from the United States Patent Office in a counterpart foreign application mailed May 31, 2002, which is less than 3 months from this filing. Also enclosed is a form PTO-1449 for the Examiner's convenience in making such consideration of record. Inclusion herein of any particular item of information is not to be construed as an admission that same is prior art.

The Commissioner is hereby authorized to charge any fees that may be required or credit any overpayment to Deposit Account 18-1167.

Respectfully submitted,

David E. Bennett

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Date: June 12, 2002
File No.: 4015-815

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FORM PTO-1449

**INFORMATION DISCLOSURE CITATION
IN AN APPLICATION PURSUANT TO
37 CFR §1.97 & 1.98**

Docket Number: 4015-815	Serial Number: 09/800,145
Applicant: Dent et al.	
Filing Date: March 6, 2001	Group: 2819

U. S. PATENT DOCUMENTS

Examiner						Filing Date	
Initial	Patent No.	Date	Name	Class	Subcl	If Approp.	
A	5,828,590	1998	Chen et al.				

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FOREIGN PATENT DOCUMENTS

							Translation	
		Patent No.	Date	Name	Class	Subcl	Yes	No

OTHER DOCUMENTS (including author, title, date, pages, etc.)

B	Bosselaers, Antoon; Govaerts, Rene; Vandewalle, Joos; "Comparison of three modular reduction functions," Crypto '93, pp. 175-186
C	Wang, Charles C.; Truong, T.K.; Shao, Howard M.; Deutsch, Leslie J.; Omura, Jim K.; Reed, Irving S.; "VLSI Architectures for Computing Multiplications and Inverses in $GF(2^m)$," IEEE Transactions on Computers, Vol. C-34, No. 8., August 1985, pp. 709-717

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation not in conformance and not considered. Include copy of this form with next communication to the applicant.